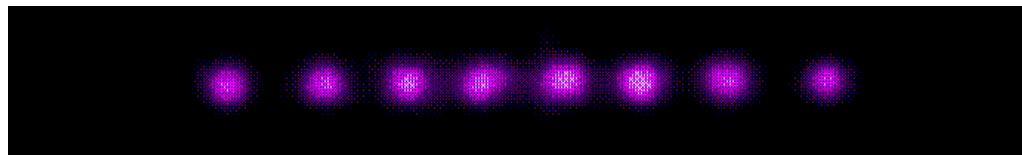


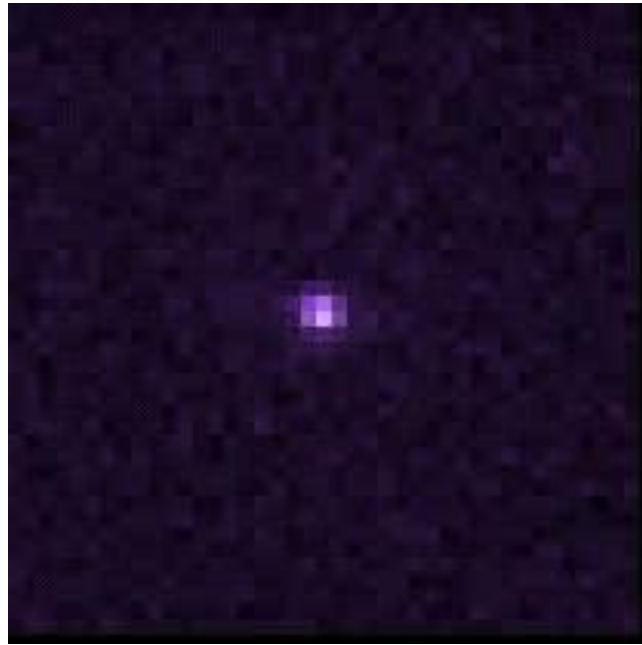
Quantum logic with trapped-ion qubits

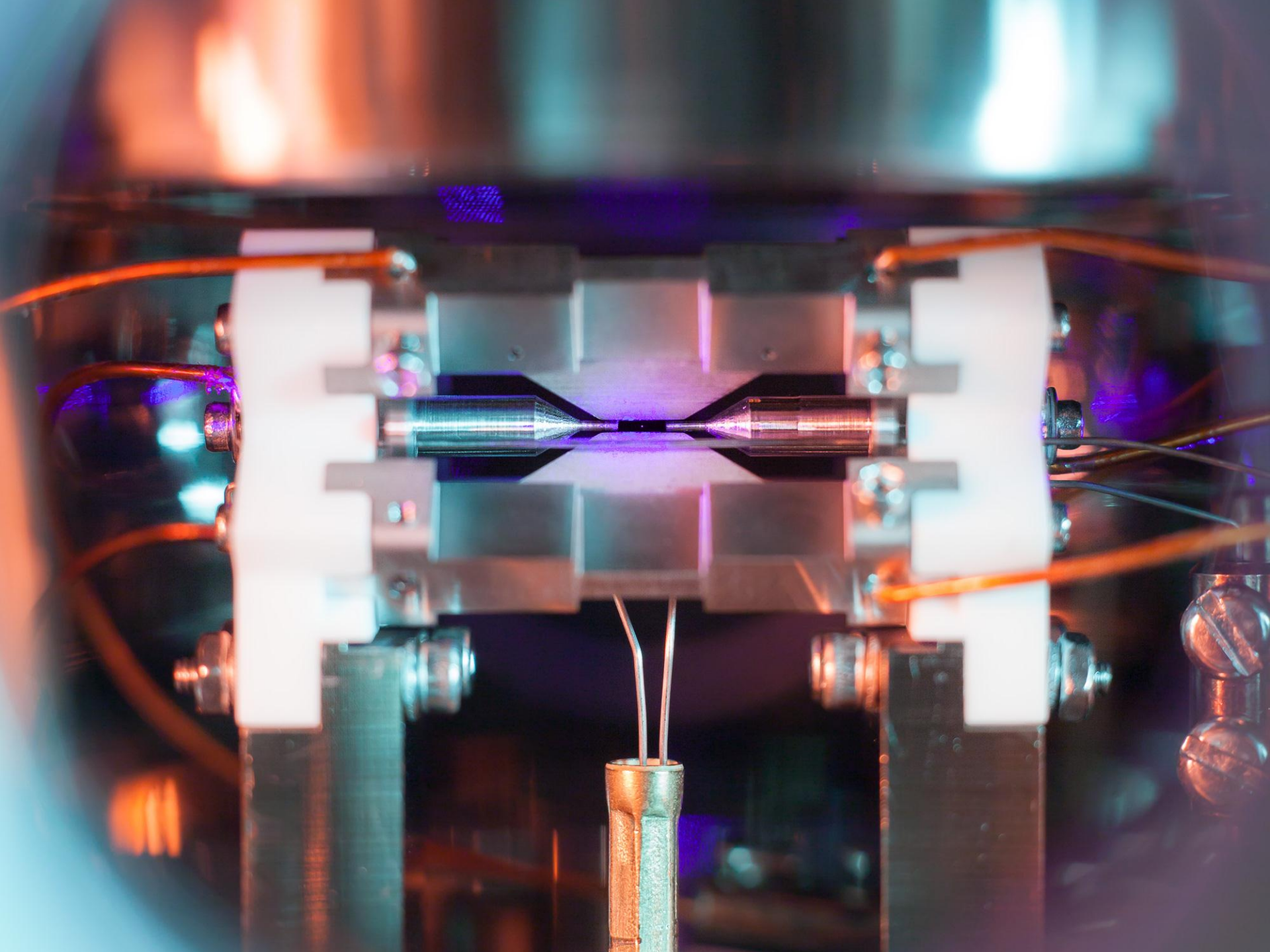
David Lucas

Clarendon Laboratory & Balliol College
Oxford University



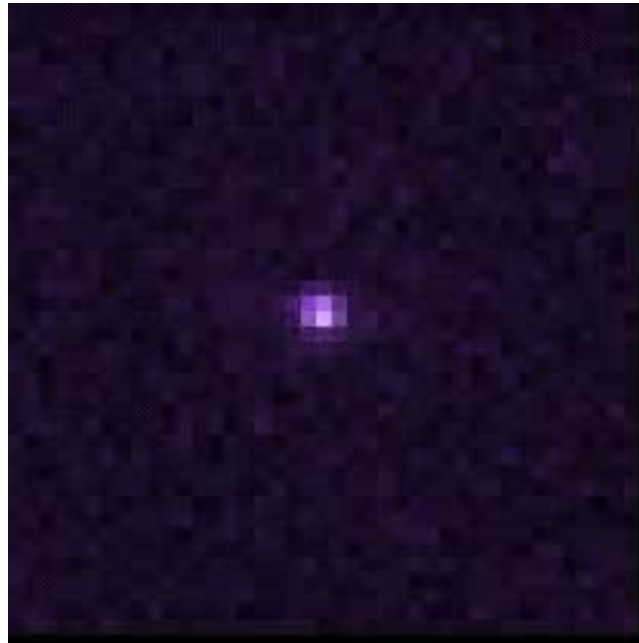
www.physics.ox.ac.uk/users/iontrap





A quantum bit ("qubit")

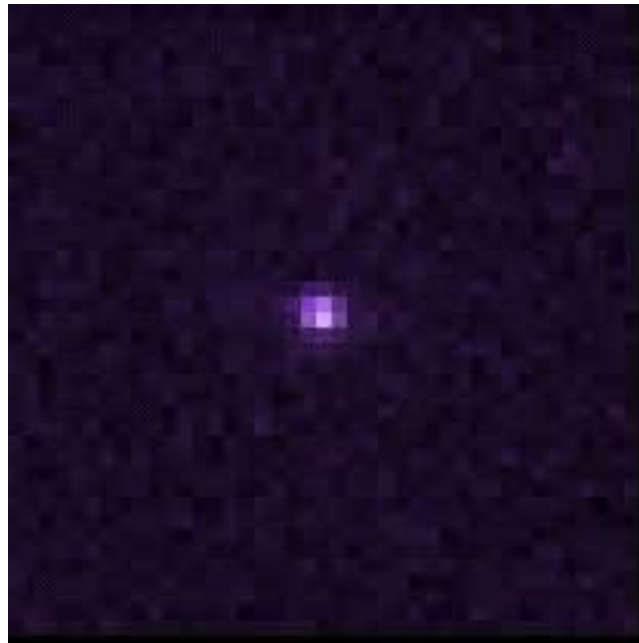
- single $^{40}\text{Ca}^+$ ion
- confined in an ion trap
- laser-cooled to $<0.001\text{K}$



$^{40}\text{Ca}^+$ ground state:
 $1s^2 2s^2 2p^6 3s^2 3p^6 4s^1$

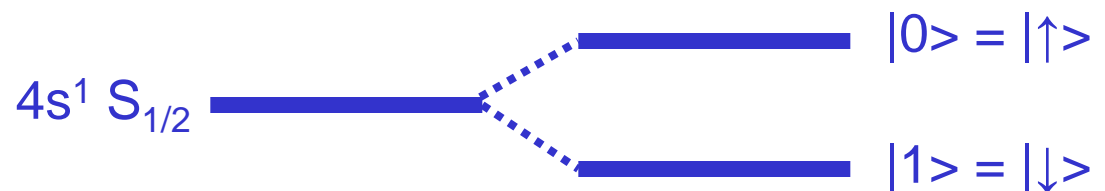
A quantum bit ("qubit")

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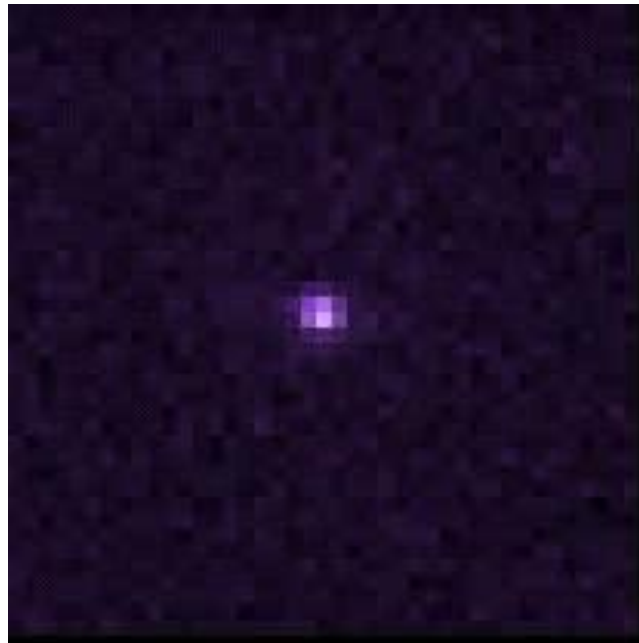
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Qubit states

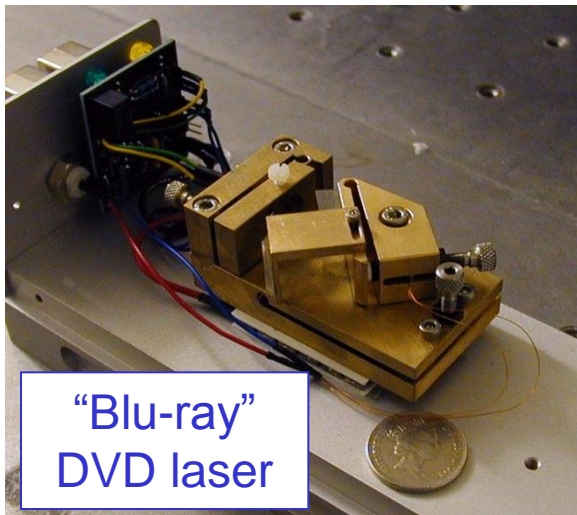


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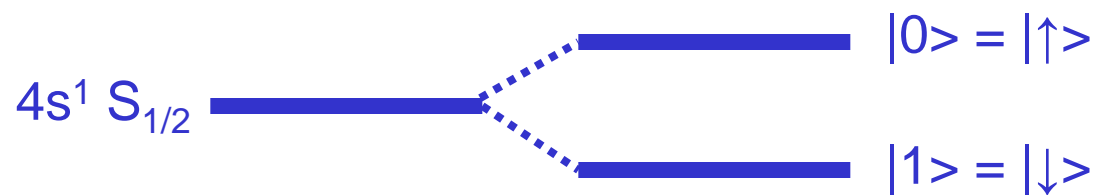


$^{40}\text{Ca}^+$ ground state:
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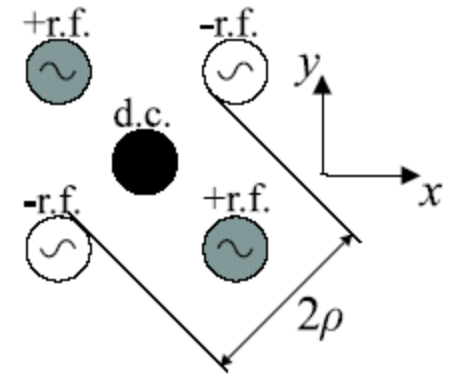
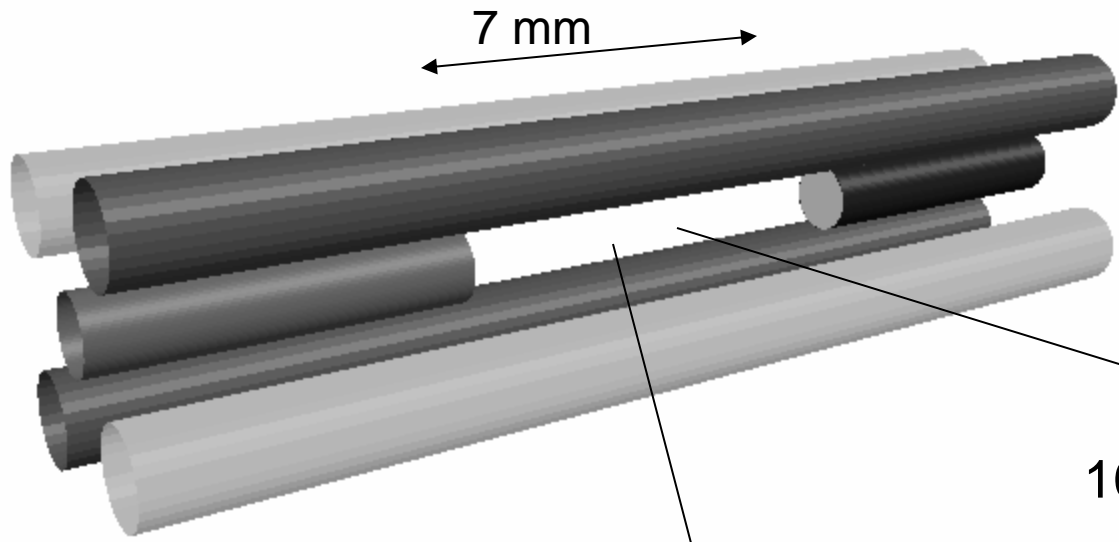


"Blu-ray"
DVD laser

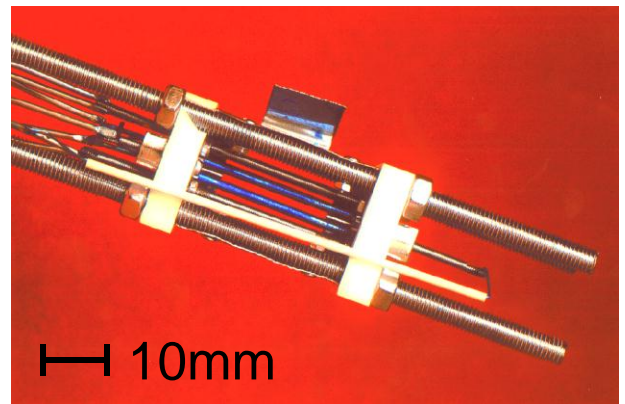
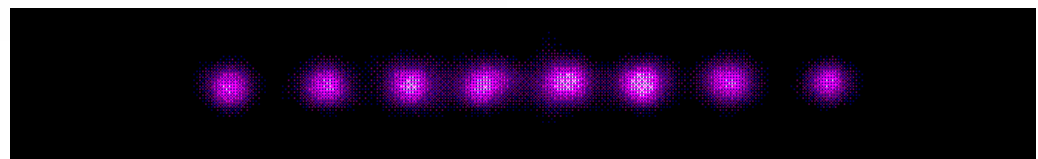
Qubit states



Ion trap "quantum abacus"



10 μ m



ion-electrode distance = 1.2 mm
motional frequencies \sim 1 MHz

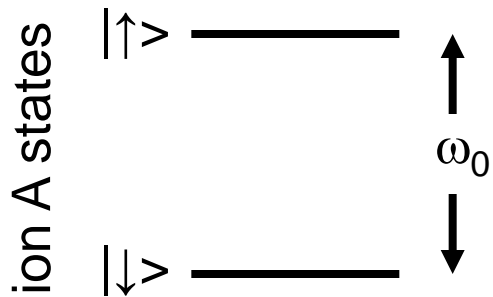
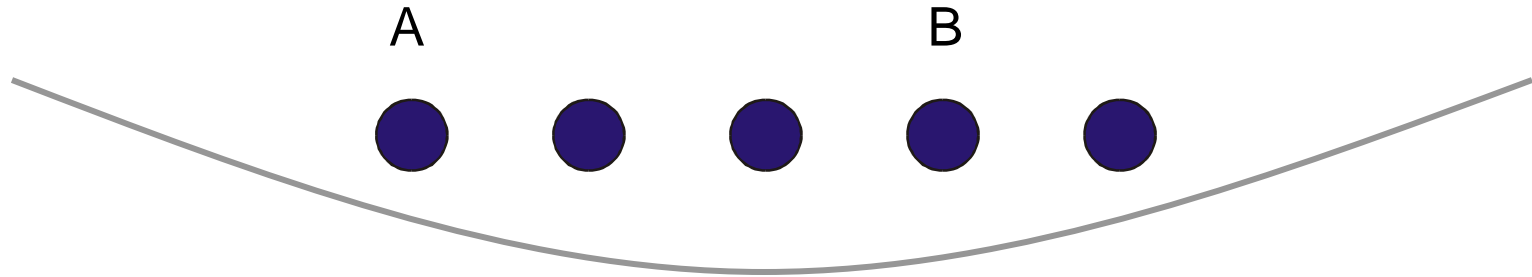
Real atoms!



~250µm

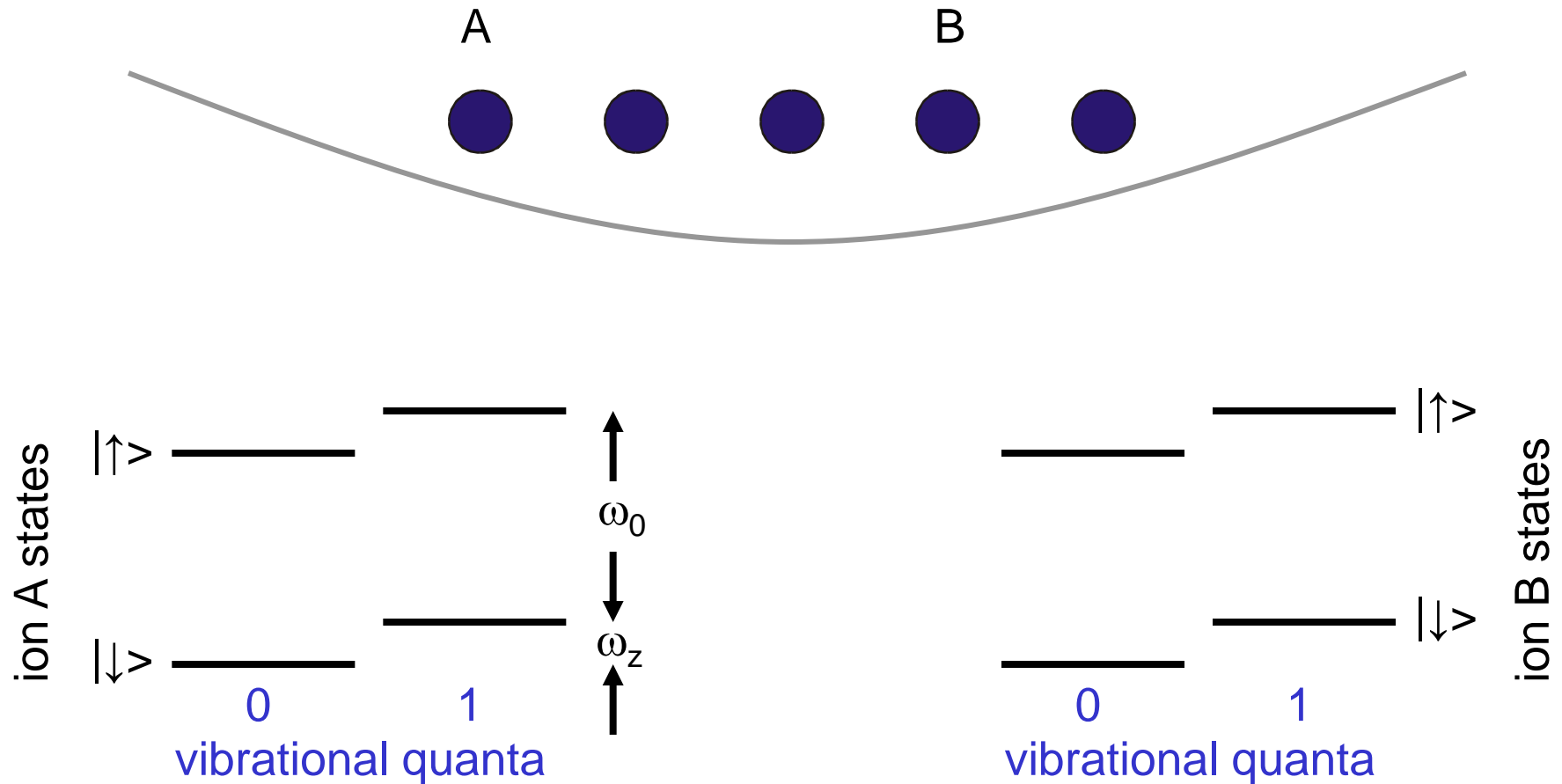
Quantum logic gate

“Controlled-NOT” gate:
flip ion B between states $|\uparrow\rangle \leftrightarrow |\downarrow\rangle$
if and only if ion A is in state $|\uparrow\rangle$



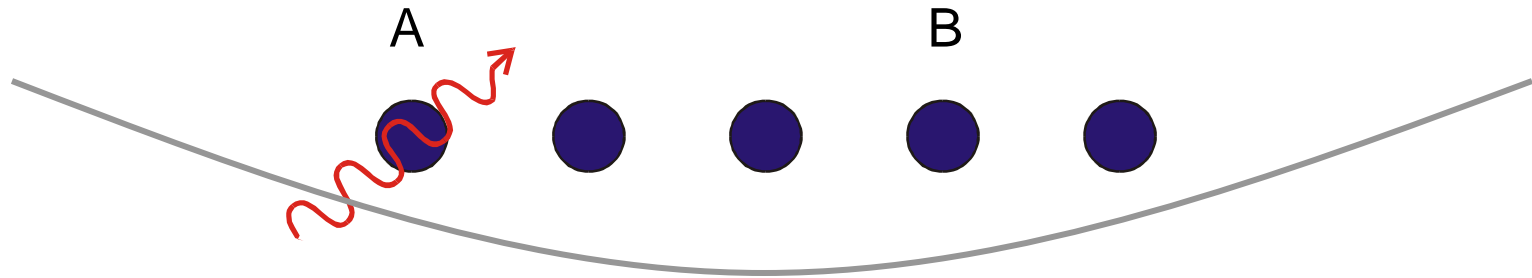
Quantum logic gate

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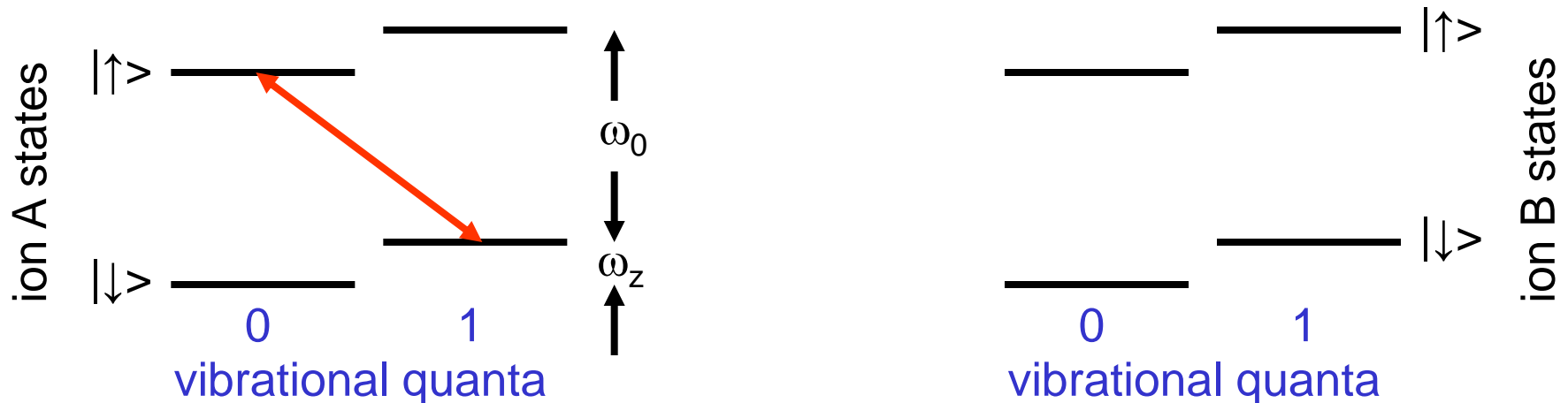


Quantum logic gate

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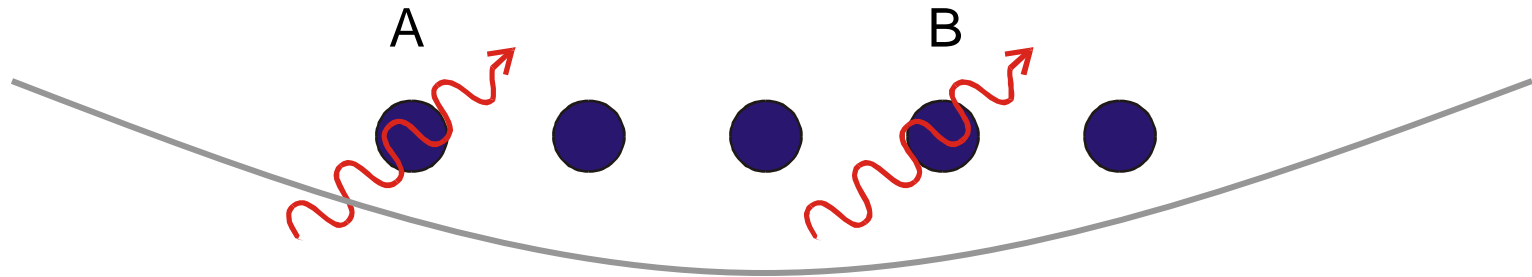


STEP (i): $\omega_0 - \omega_z$



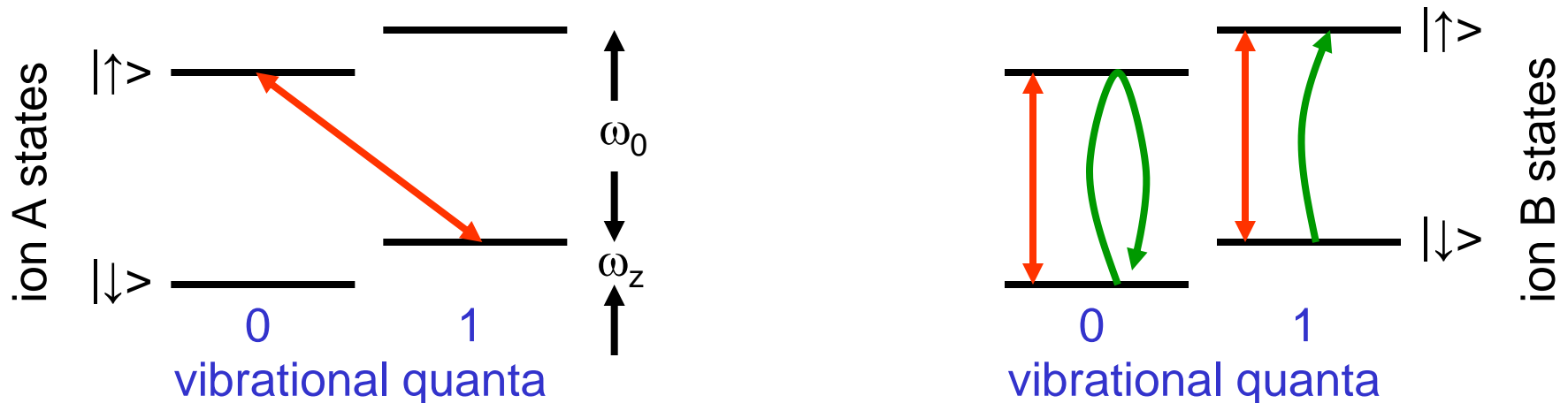
Quantum logic gate

“Controlled-NOT” gate:
flip ion B between states $|\uparrow\rangle \leftrightarrow |\downarrow\rangle$
if and only if ion A is in state $|\uparrow\rangle$



STEP (i): $\omega_0 - \omega_z$

STEP (ii): ω_0



A bit of history

1985 Idea of quantum computer (Deutsch, Oxford; Feynman, Cal.Tech.)



BBC microcomputer 1984

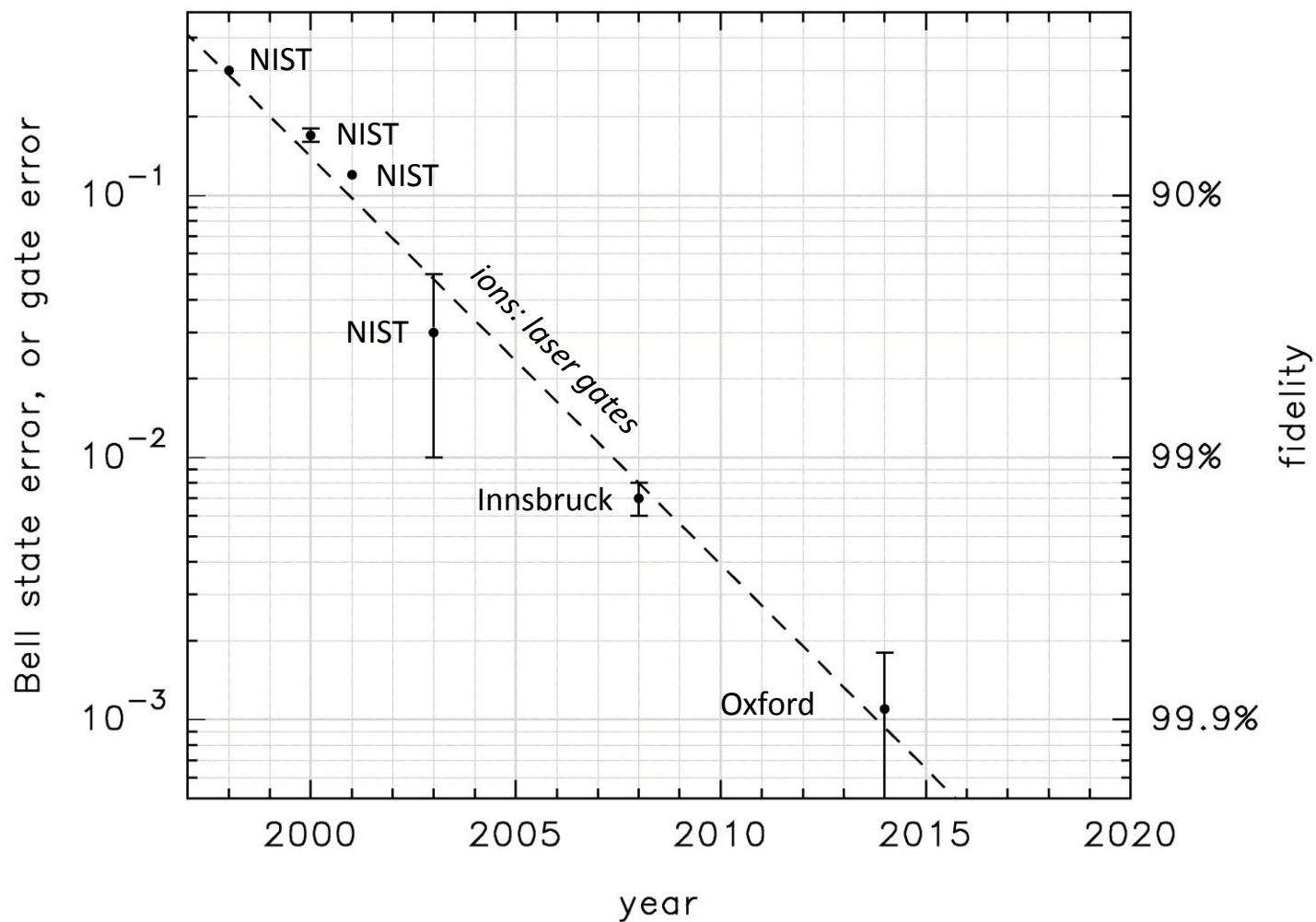
32 kbytes RAM

2 MHz clock speed

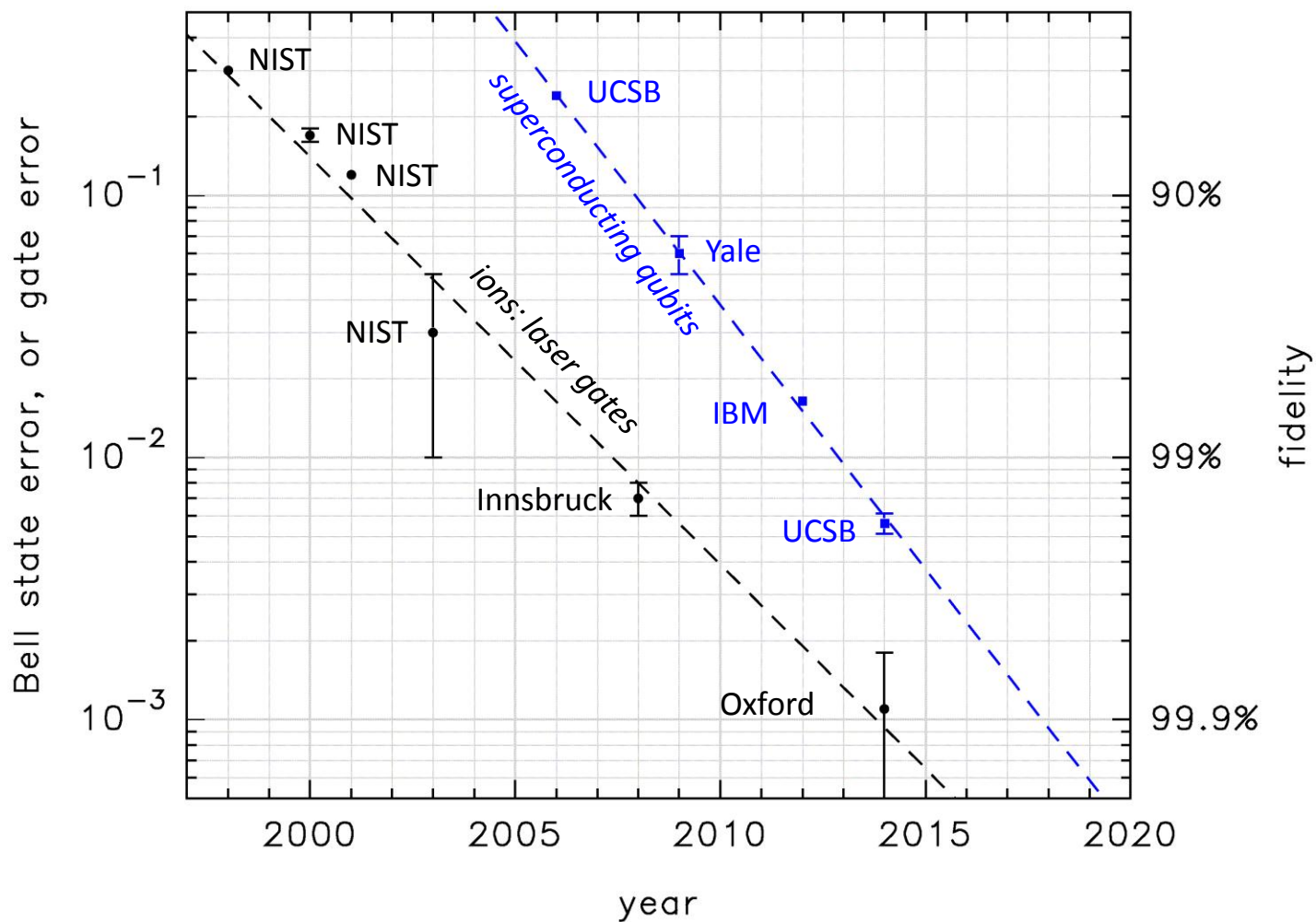
1995 Quantum error-correction invented (Shor, IBM; Steane, Oxford)

1998 First quantum logic gate demonstrated (Wineland group, NIST)

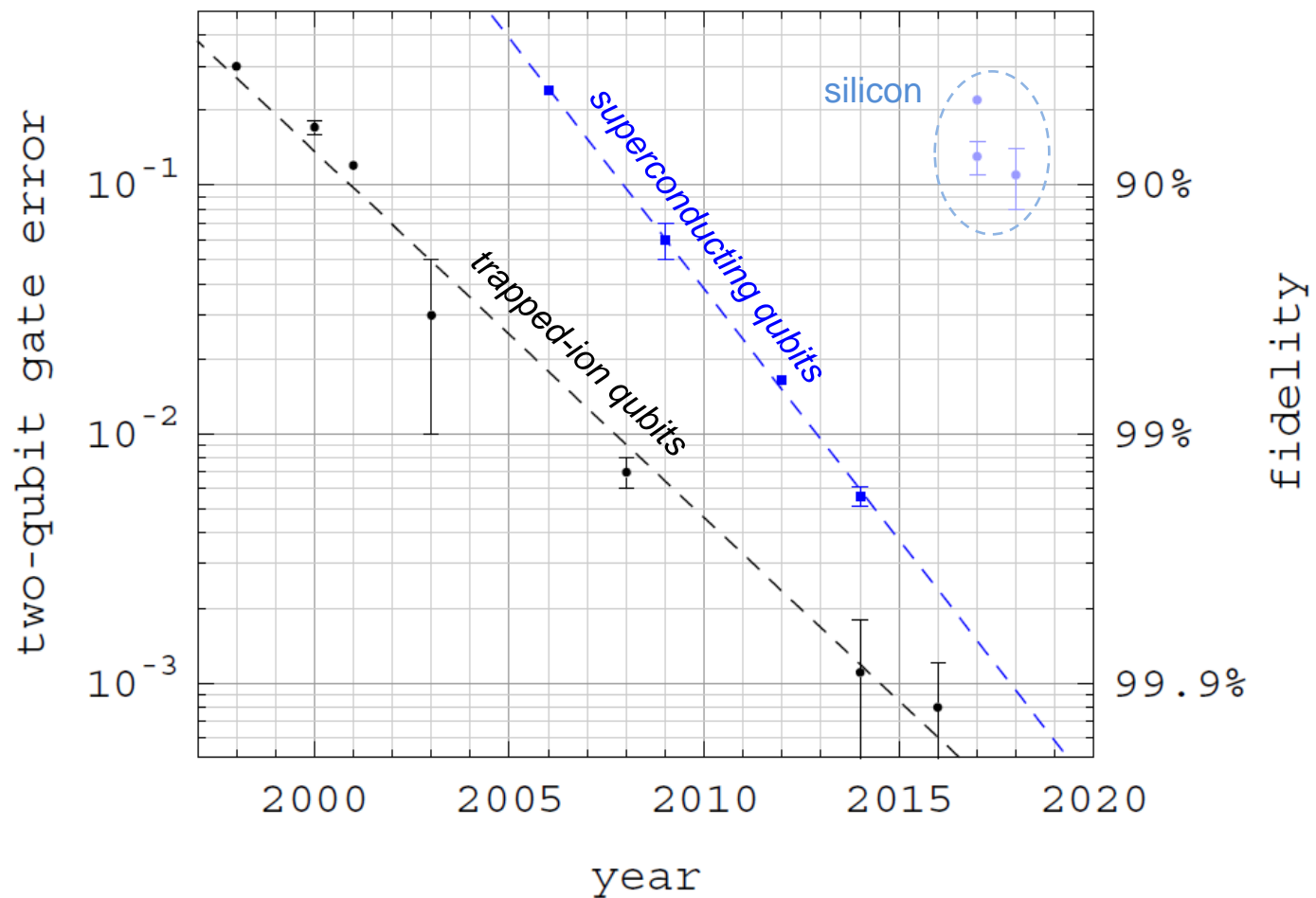
History of 2-qubit logic gates



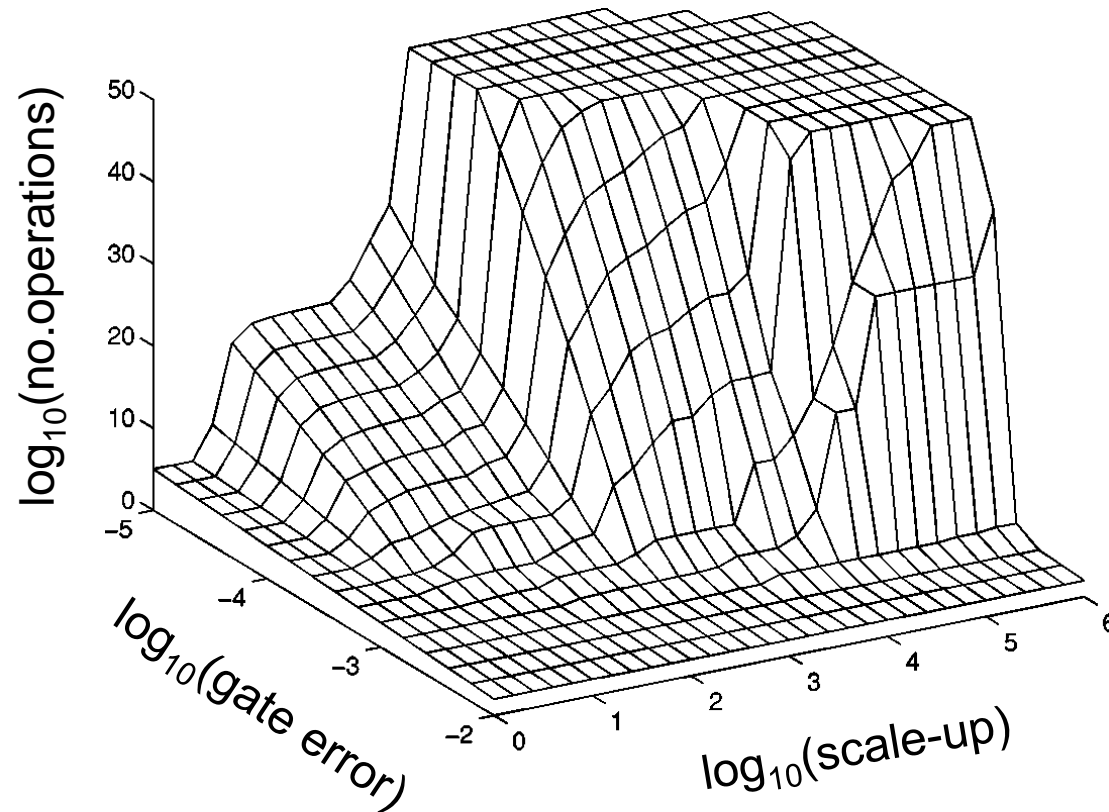
History of 2-qubit logic gates



History of 2-qubit logic gates



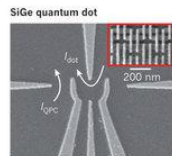
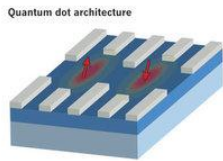
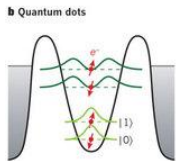
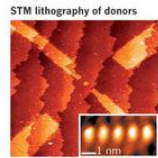
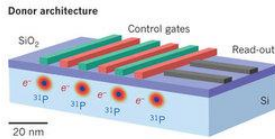
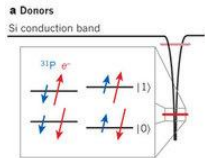
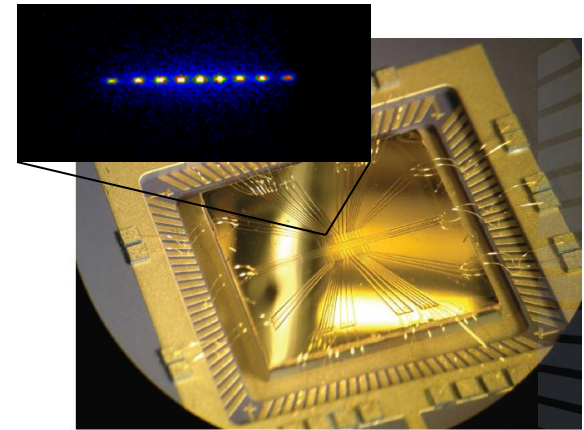
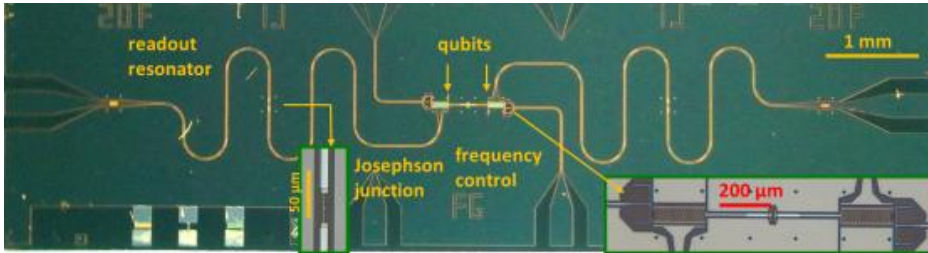
The challenge: quantum error correction



Overhead and noise threshold of fault-tolerant quantum error correction,
A.M.Steane, PRA 2003

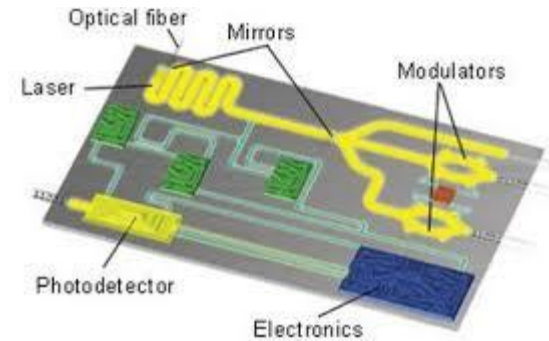
Example qubit hardware platforms

superconducting circuits



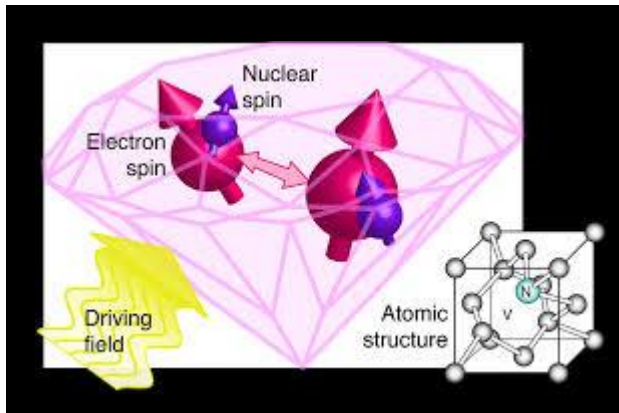
electronic semiconductor qubits

ion traps



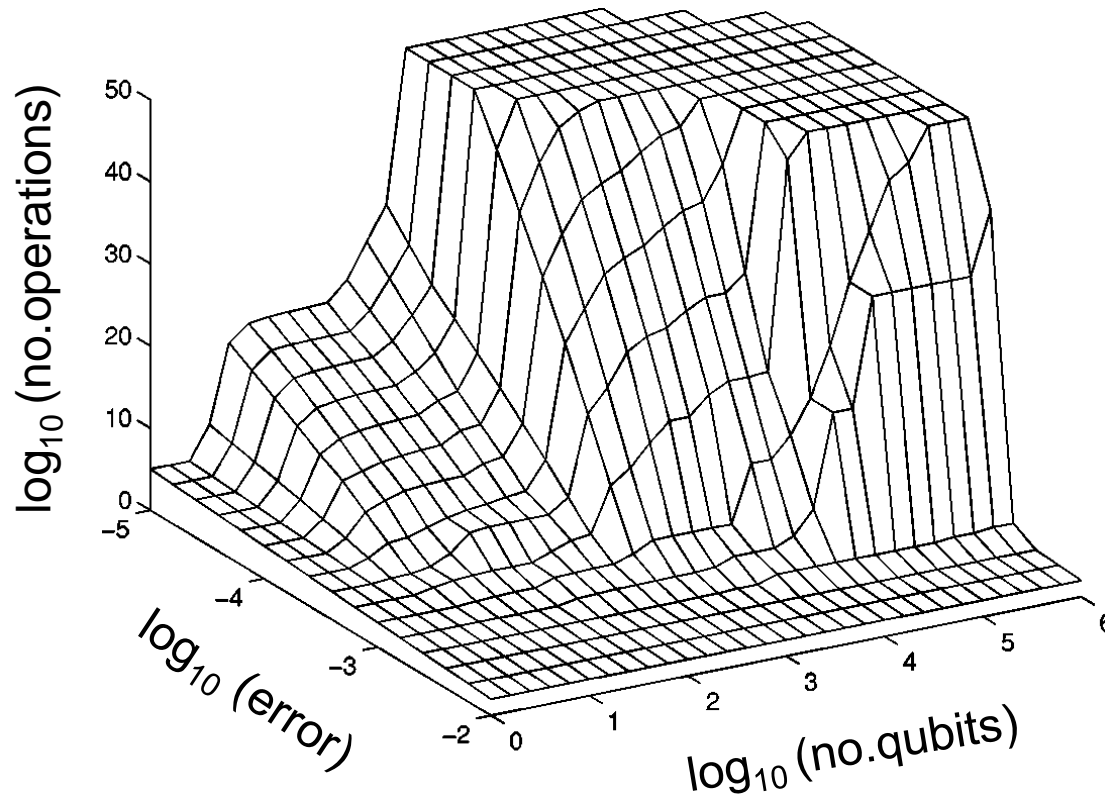
linear optics

impurity spins
in solids
(e.g. NV centres)



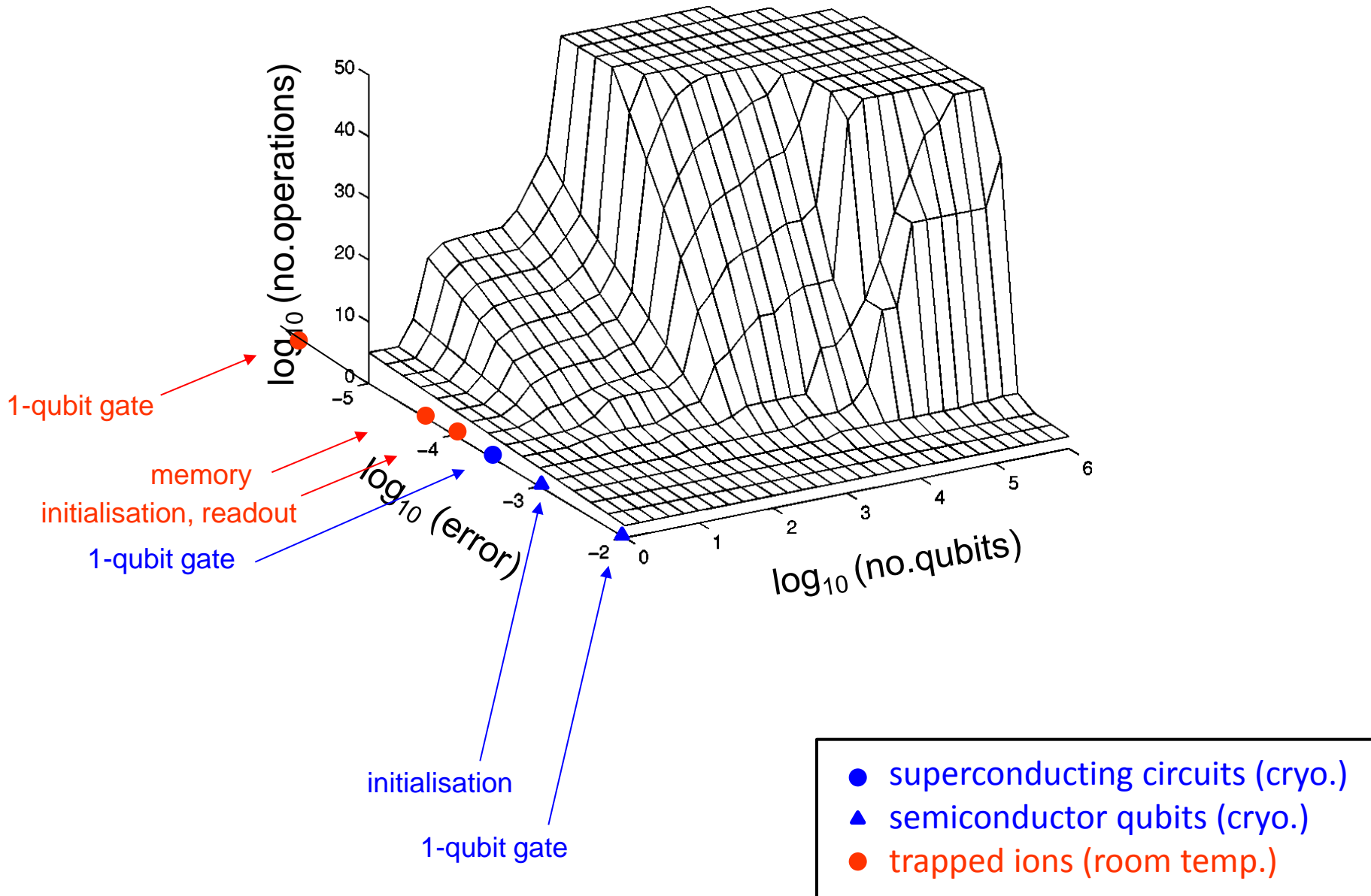
blue: usually cryogenic
red: usually room temp.

Some examples of state-of-the-art

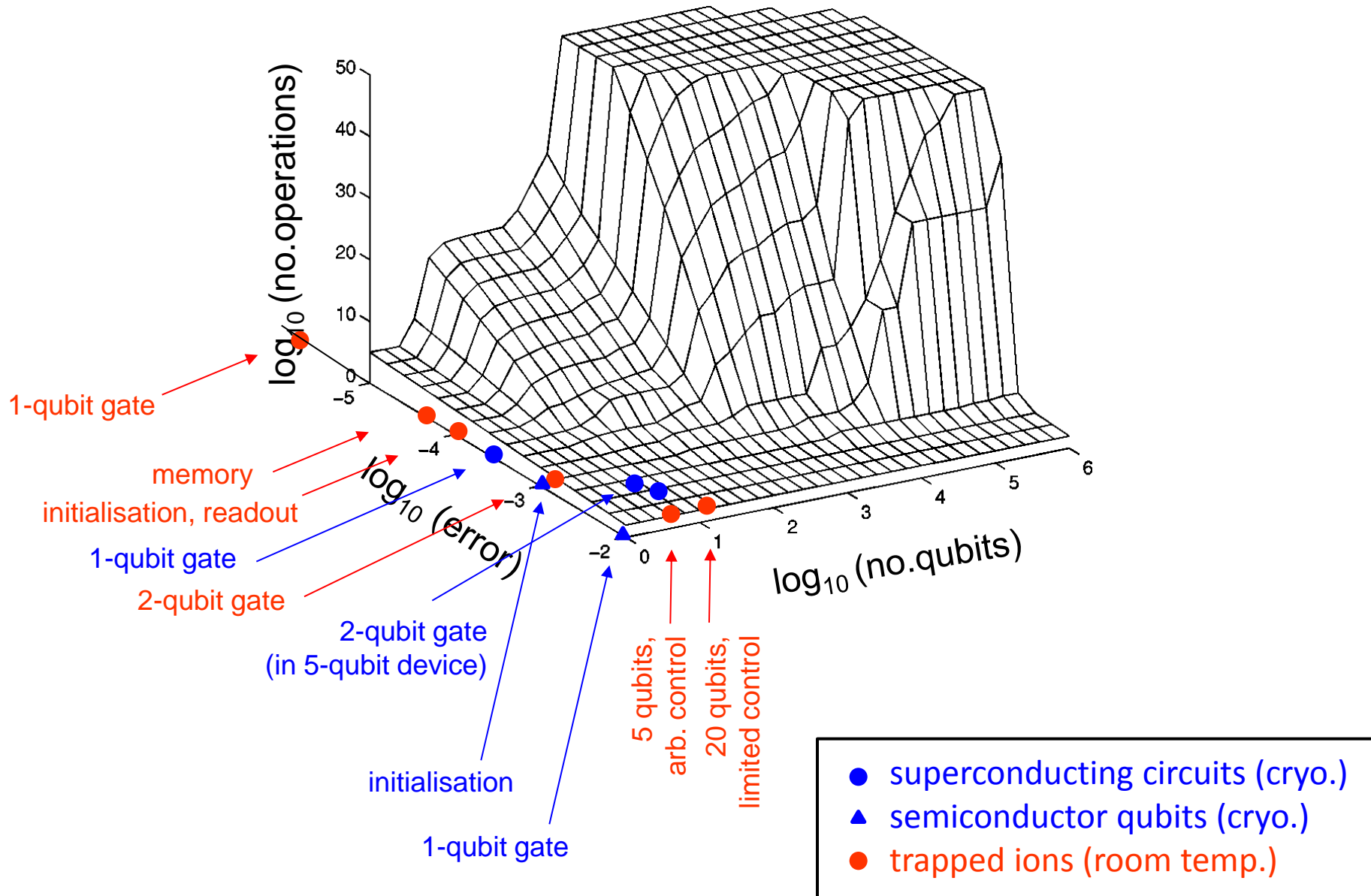


- superconducting circuits (cryo.)
- ▲ semiconductor qubits (cryo.)
- trapped ions (room temp.)

Some examples of state-of-the-art

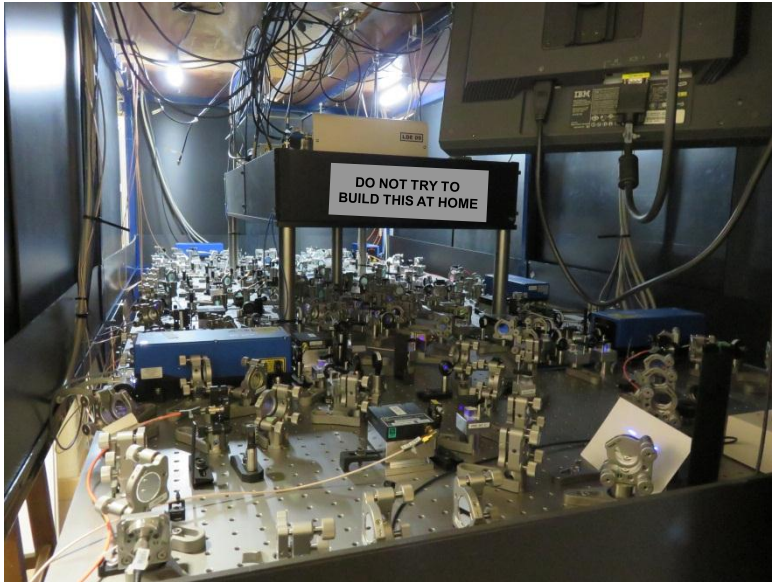


Some examples of state-of-the-art

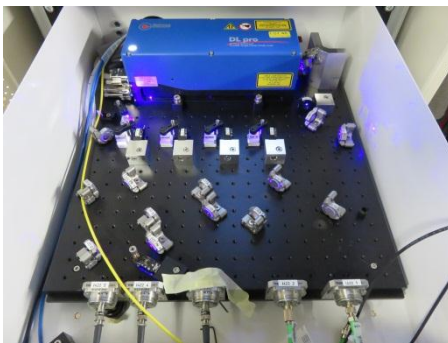
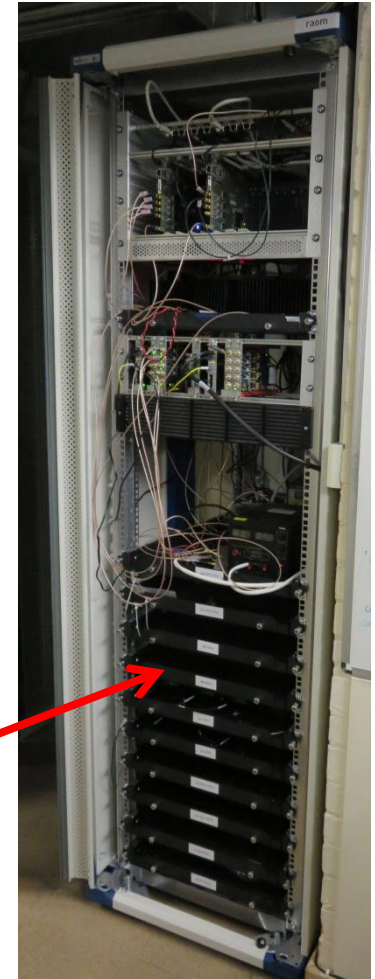


Meanwhile, in the basement...

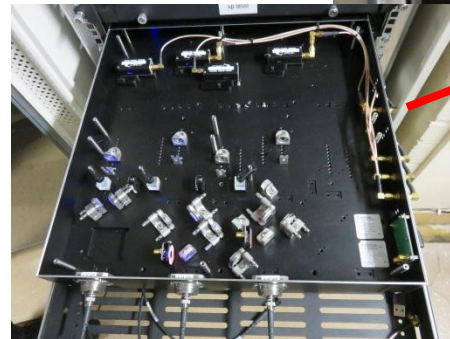
OLD: "traditional" optical table laser setup



NEW: rackmount laser system

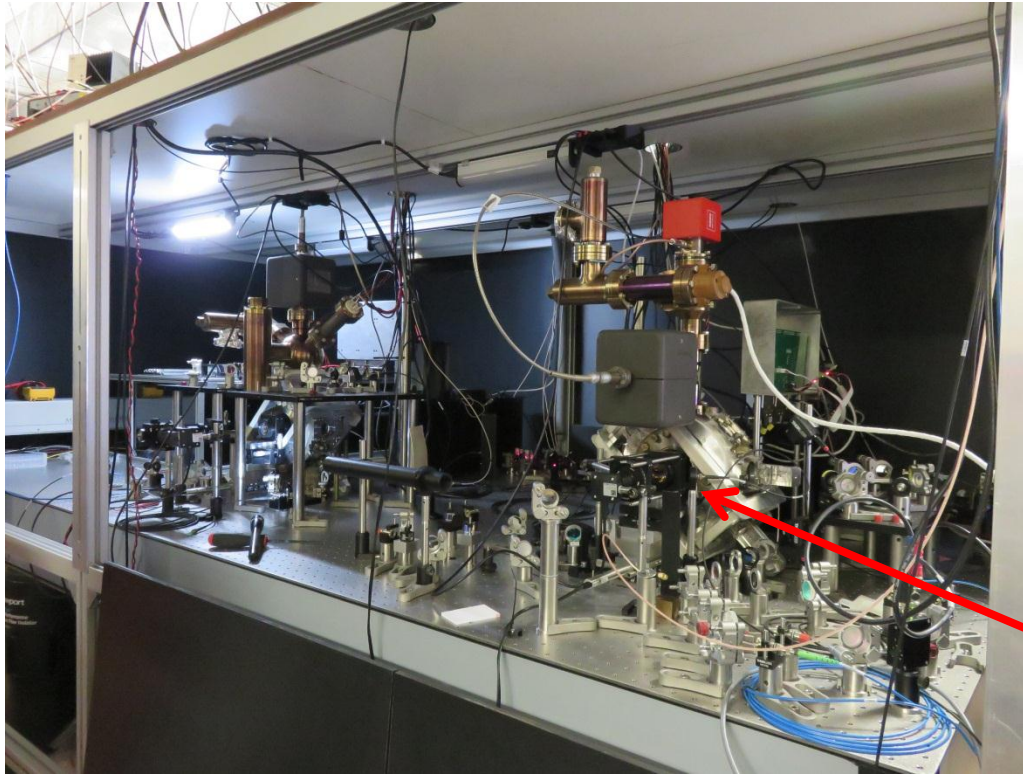


laser subsystem



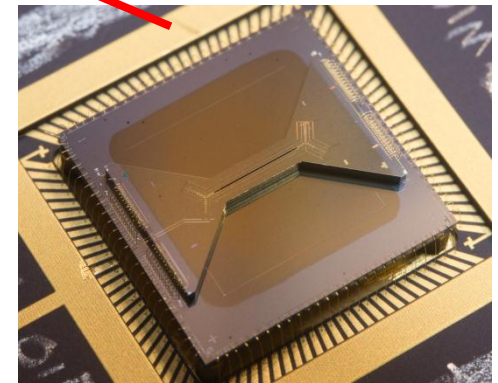
AOM subsystem

Optically-linked ion traps



Node #2 "Bob"

Node #1 "Alice"



HOA-2 surface trap
(Sandia National Labs, USA)

